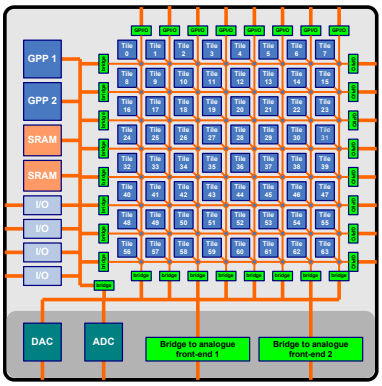
Architecture oriented paraLlelization for high performance embedded Multicore systems using scilAb

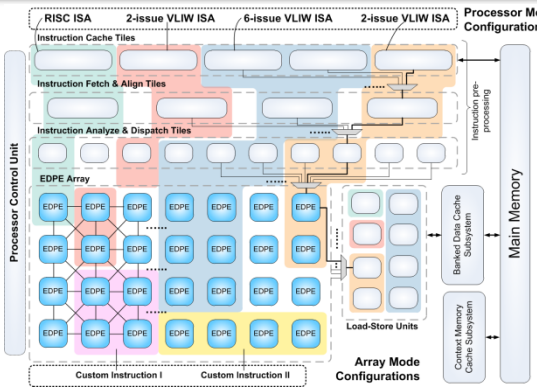
Key Features

- ✓ Automatic parallelization of applications developed with high level design tools
- ✓ Holistic ALMA toolset enabling fast and efficient development of applications for multiprocessor hardware
- ✓ Support for scalable embedded multiprocessor hardware featuring high performance embedded applications from image processing and telecommunication domain

Multicore Architectures

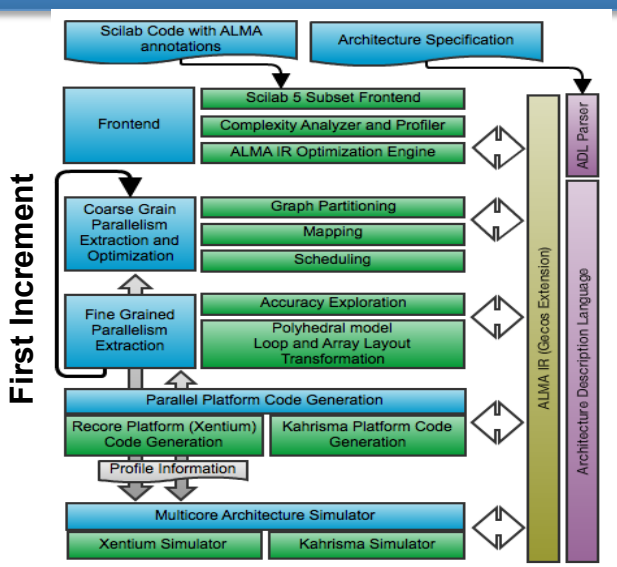


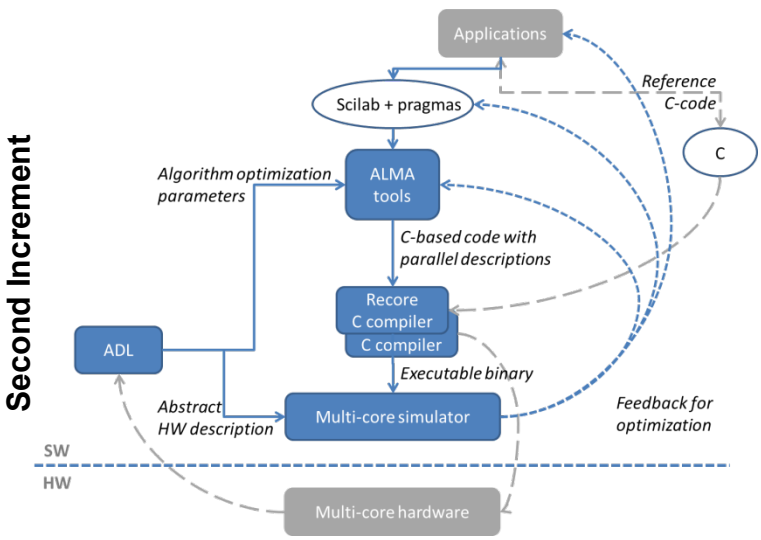
RECORE



KAHRISMA (KIT)

Paralellization Toolset





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Project website
http://www.alma-project.eu

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Université de Rennes I	France
Recore Systems B.V.	Netherlands
University of Peloponnese	Greece
Technological Educational Institute of Messolonghi	Greece
Intracom SA Telecom Solutions	Greece
Fraunhofer Institute of Optronics, System Technologies and Image Exploitation	Germany